

Fig. 1A

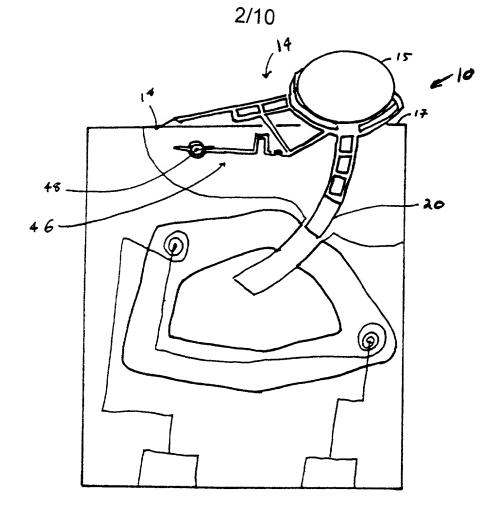


Fig. 1B

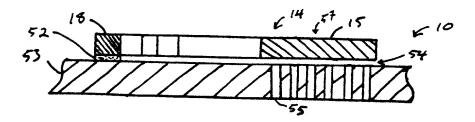


Fig. 1C

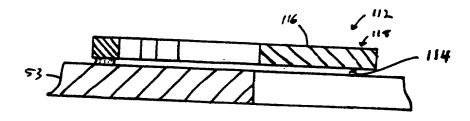


Fig. 1D

Provide SOI wafer

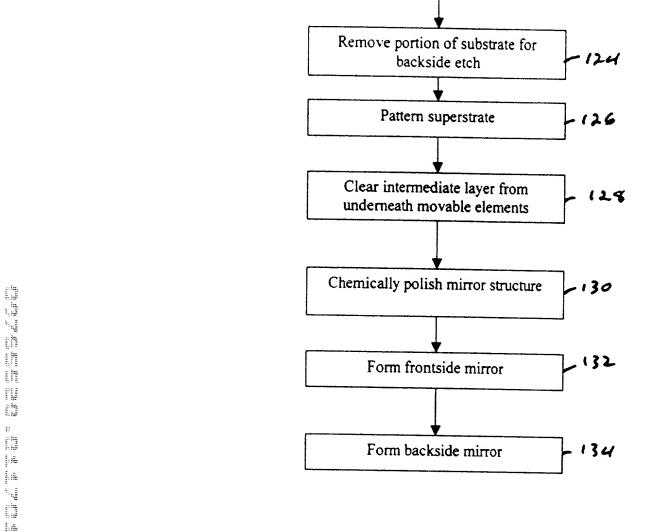


Fig. 1E

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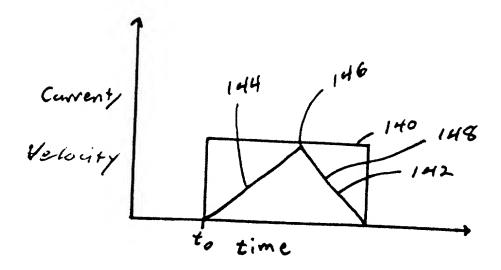


Fig. 1F

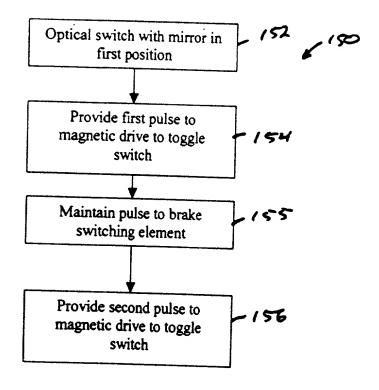
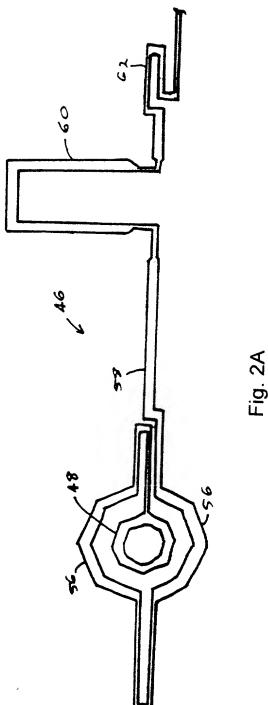
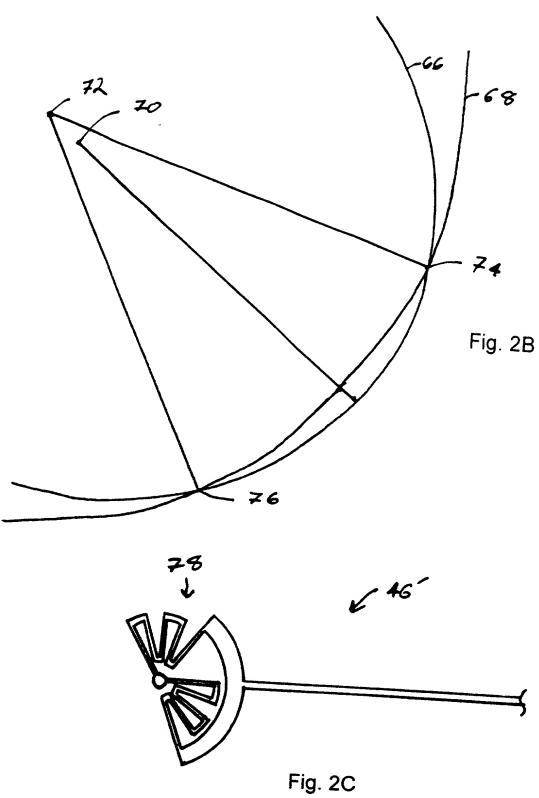


Fig. 1G





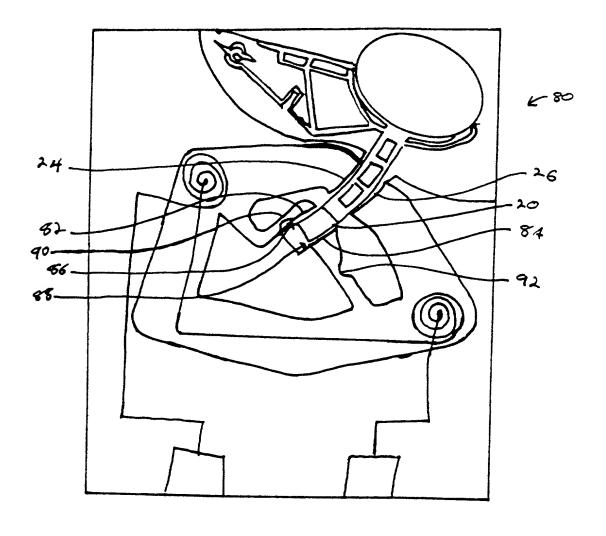


Fig. 3A

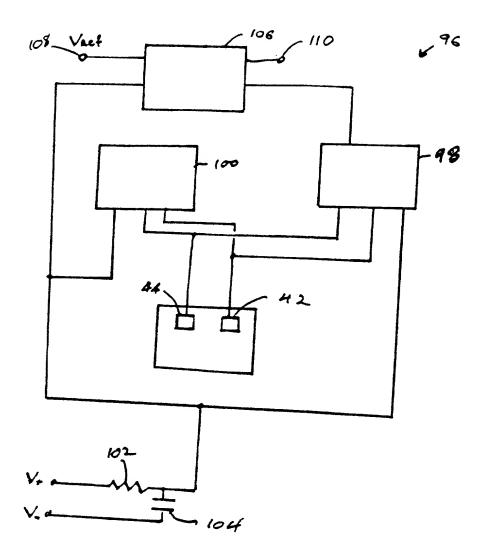


Fig. 3B

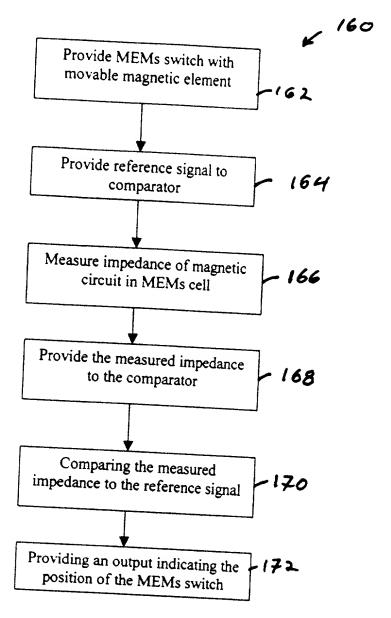


Fig. 3C

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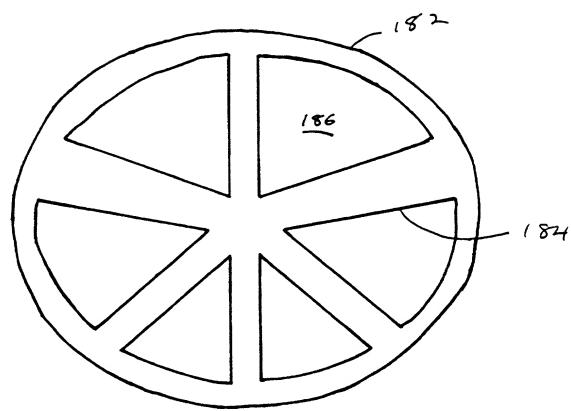


Fig. 4A

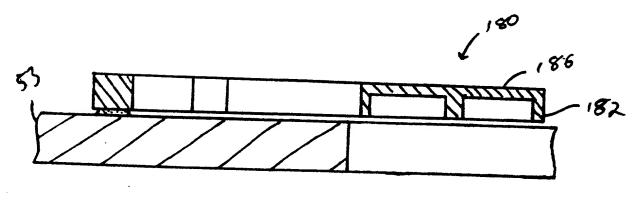


Fig. 4B